

PCN Number:	20160330001	PCN Date:	3/30/2016
Title:	Datasheet for INA3221		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



INA3221

SBOS576B –MAY 2012–REVISED MARCH 2016

Changes from Revision A (June 2013) to Revision B

Page

• Added Device Information and ESD Ratings tables, and Feature Description, Device Functional Modes, Application and Implementation, Power Supply Recommendations, Layout, Device and Documentation Support, and Mechanical, Packaging, and Orderable Information sections	1
• Deleted trace from SDA to SCL, and added missing connector dot to V _S in front-page diagram	1
• Added (V _{IN+}) + (V _{IN-}) / 2 to common-mode analog inputs in the Absolute Maximum Ratings table	5
• Deleted VBUS from analog inputs in Absolute Maximum Ratings table	5
• Added operating temperature to Absolute Maximum Ratings table	5
• Changed all V _{SENSE} to V _{SHUNT} throughout data sheet for consistency	6
• Changed "Status register" to "Mask/Enable register" to clarify register name in Basic ADC Functions section	11
• Changed Critical Alert section text for clarity	12
• Added Summation Control Function section	12
• Changed external "R _{PU} " to "R _{PU_ext} " in Figure 22	14
• Changed Multiple Channel Monitoring section text for clarity	17
• Added X and Y axis labels to Figure 25	18
• Changed "bidirectional" to "I/O" in second paragraph of Bus Overview section	20
• Changed V _{S+} to V _S in Table 1	20
• Changed references in Figure 30 to point to correct notes	22
• Changed Figure 31	23
• Changed values in Table 2, Bus Timing Definitions	23
• Added data valid time to Table 2, Bus Timing Definitions	23
• Changed fall time to split data and clock times in Table 2, Bus Timing Definitions	23
• Deleted rise time for data in Table 2, Bus Timing Definitions	23
• Deleted trace from SDA to SCL, and added missing connector dot to V _S in Figure 53	36

The datasheet number will be changing.

Device Family	Change From:	Change To:
INA3221	SBOS576A	SBOS576B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/INA3221>

Reason for Change:

To more accurately reflect device characteristics.	
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):	
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.	
Changes to product identification resulting from this PCN:	
None.	
Product Affected:	
INA3221AIRGVR	INA3221AIRGVT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
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Asia Pacific	PCNAsiaContact@list.ti.com
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