Notification Number:	20221028000.0	**Notification Date:**	October 31, 2022
Title:	Datasheet for DP83867IR/CR		
Customer Contact:	Notification Manager	**Dept:**	Quality Services
Change Type: Electrical Specification			

Description of Change:
Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.

Changes from Revision F (October 2019) to Revision G (August 2022)   **Page**

- Updated Start of Frame Detect for IEEE 1588 time stamp..1
- Added following wording to the end of first paragraph in Section 8.4.3.9 "DP83867 devices manufactured after August, 2022, have an increased random seed value that now includes 255 different seed values to expedite Auto-MDIX resolution with a link partner."..46
- Removed Reg 0x01D5 Programmable Gain Register (PROG_GAIN)..59
- Changed Bit 11:10 SPEED_OPT_ATTEMPT_CNT to RW description in 79
- Changed bits 15:9, so that bit 12 can be '1'. Bit 7 description updated Section 8.6.31 90
- Added Register 0x008A..96
- Added Register 0x00B3..97
- Added Register 0x00C0..97
- Added Register 0x0100..98

The datasheet number will be changing.

Device Family	Change From:	Change To:
DP83867IR/CR	SNLS484F	SNLS484G

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/DP83867IR

Reason for Change:
To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:
None.

Product Affected:

DP83867IRPAPR	DP83867IRPAPT	DP83867IRRGZR	DP83867IRRGZT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	**E-Mail**
WW Change Management Team	PCN_ww_admin_team@list.ti.com

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