



Product Change Notification - SYST-30DEKB779

Date:

01 Oct 2019

Product Category:

32-bit Microcontrollers

Affected CPNs:**Notification subject:**

Data Sheet - PIC32MX1XX/2XX Family Data Sheet

Notification text:

SYST-30DEKB779

Microchip has released a new Product Documents for the PIC32MX1XX/2XX Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32MX1XX/2XX Family Data Sheet](#).

Notification Status: Final

Description of Change: This revision includes the following major changes as described in Table A-10, as well as minor updates to text and formatting, which were incorporated throughout the document:

- 1) 2.0 “Guidelines for Getting Started with 32-bit MCUs: Added new section 2.11 “EMI/EMC/EFT (IEC 61000-4-4 and IEC 61000-4-2) Suppression Considerations”.
- 2) 17.0 “Serial Peripheral Interface (SPI)”; Updated information for the FRMPOL bit for Register 17-1: “SPIx CON: SPI Control Register”.
- 3) 20.0 “Parallel Master Port (PMP)”; Updated Register 20-2: “PMMODE: Parallel Port Mode Register” with a new Note 3. Updated Register Register 20-5: “PMSTAT: Parallel Port Status Register (Slave modes only)” with a new Note 1
- 4) 22.0 “10-bit Analog-to-Digital Converter (ADC)”; Updated Note 2 of FIGURE 22-1: “ADC1 Module Block Diagram”. Updated Note 4 of Register 22-4: “AD1CHS: ADC Input Select Register”

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 1 October 2019

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):

[PIC32MX1XX/2XX Family Data Sheet](#)

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Affected Catalog Part Numbers (CPN)

PIC32MX110F016B-I/ML
PIC32MX110F016B-I/SO
PIC32MX110F016B-I/SP
PIC32MX110F016B-I/SS
PIC32MX110F016B-V/ML
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