

ES_LPC84x

Errata sheet LPC84x

Rev. 1.3 — 5 November 2019

Errata sheet

Document information

Info	Content
Keywords	LPC845M301JBD64;LPC845M301JBD48;LPC845M301JHI48; LPC845M301JHI33;LPC844M201JBD64;LPC844M201JBD48; LPC844M201JHI48;LPC844M201JHI33; LPC84x errata
Abstract	This errata sheet describes both the known functional problems and any deviations from the electrical specifications known at the release date of this document. Each deviation is assigned a number and its history is tracked in a table.



Revision history

Rev	Date	Description
1.3	20191105	<ul style="list-style-type: none">Updated product identification markings.
1.2	20191008	<ul style="list-style-type: none">Updated product identification markings.
1.1	20180302	<ul style="list-style-type: none">Added ROM.1
1.0	20170614	Initial version

Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

1. Product identification

The LPC84x LQFP64 package has the following top-side marking:

- First line: LPC84xMy01
 - y: 3 or 2
- Second line: xxxxxx
- Third line: xxxyywwx[R]x
 - yyww: Date code with yy = year and ww = week.
 - xR = Boot code version and device revision.

The LPC84x LQFP48 package has the following top-side marking:

- First line: 84xMy01
 - y: 3 or 2
- Second line: xxxxxx
- Third line: xxxyy
 - Date code with yy = year.
- Fourth line: ww[R]x
 - Date code with ww = week.
 - xR = Boot code version and device revision.

The LPC84x HVQFN48 package has the following top-side marking:

- First line: 84xMy01
 - y: 3 or 2
- Second line: xxxxxx
- Third line: xxxyywwx[R]x
 - yyww: Date code with yy = year and ww = week.
 - xR = Boot code version and device revision.

The LPC84x HVQFN33 package has the following top-side marking:

- First line: 84xMy
 - y: 3 or 2
- Second line: xxxxxx
- Third line: yywwx[R]x
 - yyww: Date code with yy = year and ww = week.
 - xR = Boot code version and device revision.

Table 1. Device revision table

Revision identifier (R)	Revision description
1A	Initial device revision with Boot ROM version 13.1

2. Errata overview

Table 2. Functional problems table

Functional problems	Short description	Revision identifier	Detailed description
ROM.1	The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command) is not functional.	'1A'	Section 3.1

Table 3. AC/DC deviations table

AC/DC deviations	Short description	Detailed description
n/a	n/a	n/a

Table 4. Errata notes

Note	Short description	Detailed description
n/a	n/a	n/a

3. Functional problems detail

3.1 ROM.1: The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command) is not functional.

Introduction:

On the LPC84x, In-System programming (ISP) calls are available for programming or reprogramming the on-chip flash memory, using the boot loader software and USART, I2C, or SPI serial port. The Boot image ISP call for I2C interface or SPI interface (I2C/SPI SH_CMD_BOOT, 0xA3) can be used to boot the application currently programmed into flash, boot address starting at 0x0.

Problem:

The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command, 0xA3) is not functional.

Work-around:

Use the Reset device ISP call for I2C interface or SPI interface (SH_CMD_RESET command, 0xA2). This command can be used to reset the LPC84x device.

4. Legal information

4.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

4.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

4.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

5. Contents

1	Product identification	3
2	Errata overview	4
3	Functional problems detail	5
3.1	ROM.1: The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command) is not functional.	5
4	Legal information	6
4.1	Definitions	6
4.2	Disclaimers	6
4.3	Trademarks	6
5	Contents	7

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2019.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 5 November 2019

Document identifier: ES_LPC84X